SYSTEM AND METHOD FOR APPROXIMATING DIVISION

ABSTRACT OF THE DISCLOSURE

A system and method are used to perform division or complex division using multiplication and/or summation devices and steps. A numerator and denominator of a complex division signal are filtered. A separate determination of their values is performed using separate logic systems. The separate values are multiplied together for form an output signal. The denominator logic system converts the complex division signal into a signal that is processed using multiplication and summation devices. The processing adjusts an approximated past value using an error value. The error value can be based on a present value, a past value, and a scaling coefficient.

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